



2811 #7  
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Docket No.: GR 00 P 4121

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By: [Signature]

Date: 10/22/01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Albert Birner et al.  
Applic. No. : 09/871,013  
Filed : May 31, 2001  
Title : Method for Making Electrical Contact with a Rear Side of a Semiconductor Substrate During its Processing  
Art Unit : 2811

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,  
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

U.S. Patent 5,911,461 (Sauter et al.), dated June 15, 1999;

U.S. Patent 5,463,526 (Mundt), dated October 31, 1995;

U.S. Patent 4,876,224 (Kusakabe), dated October 24, 1989;

European Patent Application EP 0 806 798 A2 (Harvey), dated November 12, 1997;

U.K. Patent Application 2 171 556 A (Yasuhiro et al.), dated August 28, 1986;

U.K. Patent Application 2 149 697 A (Armand et al.), dated June 19, 1985;

Respectfully submitted,

[Signature]  
For Applicants

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Date: October 22, 2001

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